2023 Digital IC Design Homework 1

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| NAME | | 黃彥承 | | | | | | |
| Student ID | | N26101185 | | | | | | |
| **Functional Simulation Result** | | | | | | | | |
| Stage 1 | Pass | | Stage 2 | Pass | Stage 3 | Pass/ | Stage 4 | Pass |
| **Stage 1** | | | | | | | | |
| (your simulation result) | | | | | | | | |
| **Stage 2** | | | | | | | | |
| (your simulation result) | | | | | | | | |
| **Stage 3** | | | | | | | | |
| (your simulation result) | | | | | | | | |
| **Stage 4** | | | | | | | | |
| (your simulation result) | | | | | | | | |
| **Description of your design** | | | | | | | | |
| 在這次的 8-inputs MMS，和 4-inputs MMS 實作中，結構依上至下主要分成: 8-inputs MMS、4-inputs MMS、2-inputs MMS。  4-inputs MMS: number0~1 和 number2~3 兩兩一對平行送入兩個先前定義好得 2-inputs MMS，最後比較出來的兩個值，再送入最後的 2-inputs MMS 已獲的最終的最大或最小值。  8-inputs MMS: number0~3 和 number3~7 四個一組平行送入兩個先前定義好得 4-inputs MMS，最後比較出來的兩個值，再送入最後的 2-inputs MMS 已獲的最終的最大或最小值。 | | | | | | | | |